Rev. 11-3/98



## DECLARATION AND POWER OF ATTEMPT FOR U.S. PATENT APPLICATION

(X) Original () Supplemental () Substitute () PCT () DESIGN

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

#### Title: WAVEFORM EQUALIZATION CONTROLLER AND WAVEFORM EQUALIZATION CONTROL METHOD

THE WAVEFORM EQUALIZATION CONTROLLER	AND WAVELORM EQUALIZATION CONTROL METHOD
of which is described and claimed in:	
() the attached specification, or	
(X) the specification in application Serial Noapplicable), or	, filed October 30, 2000, and with amendments through (if
() the specification in International Application No., filed	d, and as amended on (if applicable).
I hereby state that I have reviewed and understand the co by any amendment(s) referred to above.	ntent of the above-identified specification, including the claims, as amended
I acknowledge my duty to disclose to the Patent and Trac defined in Title 37, Code of Federal Regulations, §1.56.	demark Office all information known to me to be material to patentability as
	States Code, §119 (and §172 if this application is for a Design) of any clow and have also identified below any application for patent or inventor's on on which priority is claimed:

COUNTRY	APPLICATION NO.	DATE OF FILING	PRIORITY CLAIMED
Japan	11-309872	October 29, 1999	Yes

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose information material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION SERIAL NO.	U.S. FILING DATE	STATUS: PATENTED, PENDING, ABANDONED

And I hereby appoint Michael R. Davis, Reg. No. 25,134; Matthew M. Jacob, Reg. No. 25,154; Jeffrey Nolton, Reg. No. 25,408; Warren M. Cheek, Jr., Reg. No. 33,367; Nils Pedersen, Reg. No. 33,145; and Charles R. Watts, Reg. No. 33,142, who together constitute the firm of WENDEROTH, LIND & PONACK, L.L.P., jointly and severally, attorneys to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith.

I hereby authorize the U.S. attorneys named herein to accept and follow instructions from HAYASE & CO. as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and myself. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by me.

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I further declare that all statements made herein of my own knowledge are true, and that all statements on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

1st Inventor Naoya TOKUNAGA	Date November 13, 2000
2nd Inventor <u>Kazuya Uoda</u>	Date Wovember 15, 2000
3rd Inventor	Date
4th Inventor	Date
5th Inventor	Date
6th Inventor	Date
The above application may be more particularly identified	d as follows:
U.S. Application Serial No.	Filing Date October 30, 2000
Applicant Reference Number <u>P-23754-01</u>	Atty Docket No. 2000-1507A

Title of Invention WAVEFORM EQUALIZATION CONTROLLER AND WAVEFORM EQUALIZATION CONTROL METHOD